

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 108391-00011	SERIAL NO. 09/652,023
<p style="text-align: center;"><b>LIST OF REFERENCES CITED BY APPLICANT</b> (Use several sheets if necessary)</p>			APPLICANT Yasuhiro WAKIMOTO		
			FILING DATE August 31, 2000	GROUP 2186	

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**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
whc	AA	5,802,554	9/1/1998	Cáceres et al			
whc	AB	5,845,311	12/1/1998	Piguet et al			
	AC						
	AD						
	AE						JAN 02 2004
	AF						Technology Center 2100

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**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO PART.
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

**OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)**

whc	AM	Edenfield, Robin, et al, "The 68040 On-Chip Memory Subsystem", Computer Society International Conference, IEEE Comp. Soc. Press, Vol. 35, February 1990, pp. 264-269. <i>whc</i> <i>Page 264</i>
whc	AN	Fuccio, Michael L., "The DSP32C: AT&T's Second-Generation Floating-Point Digital Signal Processor", IEEE Micro, Vol. 8, No. 6, December 1998, pp. 30-47. <i>1st page, unknown page number</i> <i>whc</i>
	AO	

EXAMINER <i>Yasuhiko Wakimoto</i>	DATE CONSIDERED <i>3/29/04</i>
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	